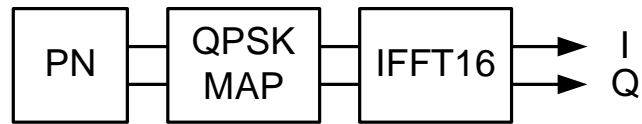
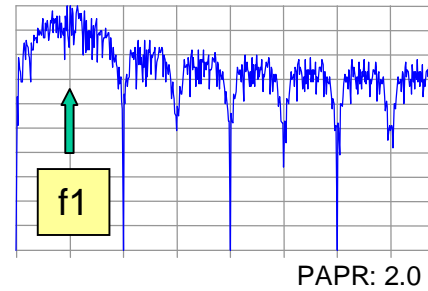


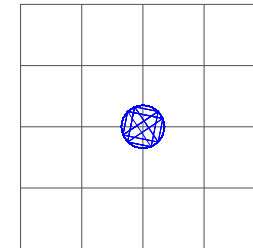
FPGA構成



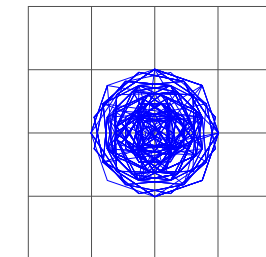
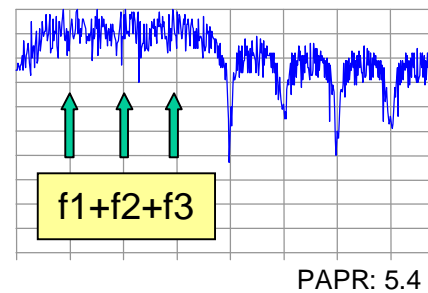
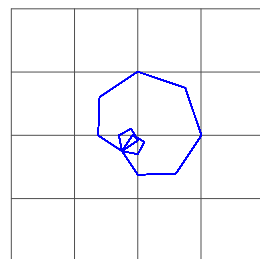
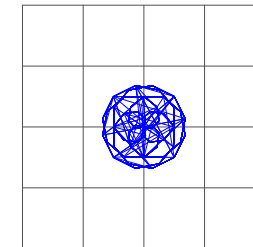
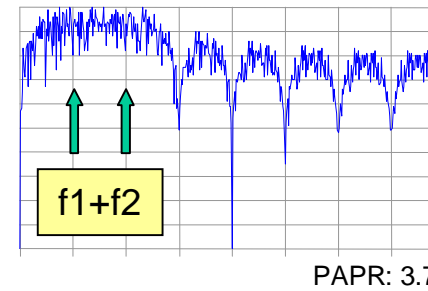
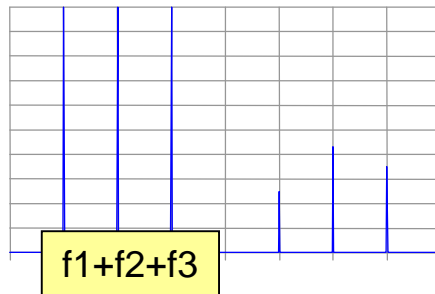
周波数特性



Constellation



無変調



## FPGA implementation

File Name: OFDM16\_15.rbt

### Signals

Port	Signals
1	IFFT output

### Control Registers

REG No	REG名称	Byte	機能
REG7	DATA Mode	1byte	1: 送信データをゼロに固定 0: 送信データ擬似ランダム
REG10	Freq. MASK	2byte	F1:4000 F2:6000 F3:7000